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		-	U.S. PATENT D	OCUMENTS		
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		FG	REIGN PATENT	DOCUMENTS	_		
						Trans	lation
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OTHER DOCUMENTS (including Author, Title, Date, Pertinent Pages, etc.)							
Ref. Desig.	Examiner's Initials						
1.	GJP	Yasuhiro TAKAI, et al., "250 Mbyte/s Synchronous DRAM Using a 3-Stage-Pipelined Architecture", IEEE JOURNAL OF SOLID-STATE CIRCUITS, VOL. 29, NO. 4, pp. 426-431 (April, 1994)					
2.	GJP	Yuji SAKAI, et al., "A Synchronous DRAM with New High-Speed I/O Lines Method for the MultiMedia Age", IEICE TRANS. ELECTRON., VOL. E78-C, NO. 7, pp. 782-788 (July, 1995)					

Examiner: GARY J. PORTKA Date

Date Considered: 3/15/04

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1.	GJP	HITACHI Hardware Manual, Hitachi SuperH RISE engine, SH7750 Series, SH7750, SH7750S, Rev. 4.0, April 21, 2000		

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1.	GJP	US 5,307,302 A	Apr. 1994	Farrer, et al.	·	
2.	GJP	US 4,172,281 A	Oct. 1979	Gordon		

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1.	-GJP	EP 0 619 546 A	Oct. 1994	EPO			

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